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MAR 29 2005

Docket No. F-6810

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kenji SHIGEKI, et al.

Serial No. : 09/772,027

Filed : January 29, 2001

For : LOGIC INTEGRATED CIRCUIT, AND RECORDING MEDIUM READABLE BY A COMPUTER, WHICH STORES A SOURCE OF CPU CORE ON SAID LOGIC INTEGRATED CIRCUIT

Group Art Unit : 2183

Examiner : William M. Treat

Confirmation No. : 9064

Customer No. : 000028107

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(Name)

H.F. Ruschmann  
(Signature)

MAIL STOP AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## AMENDMENT

Sir:

In response to the Office Action of November 29, 2004, please amend the above-identified patent application as follows:

F6810 AM01 (PC10).spd

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